





### U1 Gate A No Glitch

U1 pin 1	U1 pin 2	Reason
0 to 1	0 to 1	Don't Care
0 to 1	1 to 0	FF Clock delayed 1 uS; Clock delayed 0; Clock false before Gate true
1 to 0	0 to 1	FF Clock delayed 1 uS; Clock delayed 10 uS; Gate false before clock true
1 to 0	1 to 0	Don't Care

<b>Modular Synthesis</b>		
<b>Moon Modular 569 Companion</b>		
David J. Brown	Rev 1.3	Connectors
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